



IOL HAT Software

Application Manual

www.pinetek-networks.com

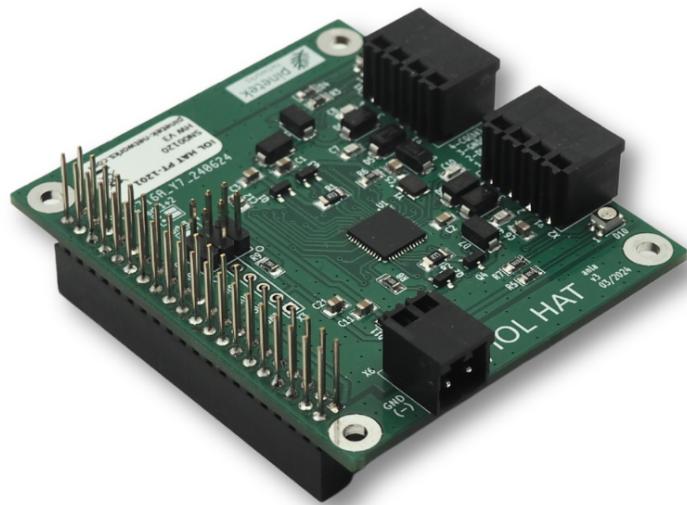


Table of Contents

1 Introduction	5
<i>Theory of operation</i>	5
<i>Hints and Warnings</i>	5
1.1 License and Disclaimer	6
<i>Software License Notice</i>	6
<i>Disclaimer of Liability</i>	6
2 IOL Master Application	7
2.1 Prepare the Target	8
<i>Enable SPI communication</i>	8
<i>GPIO</i>	8
2.2 Arguments and Timing	9
<i>Command line arguments</i>	9
<i>Timing considerations</i>	9
3 Binary protocol	10
<i>Error handling</i>	10
3.1 CMD_PWR	12
3.2 CMD_LED	13
3.3 CMD_PD	14
3.4 CMD_READ	15
3.5 CMD_WRITE	16
3.6 CMD_STATUS	17
Appendix-A Error messages	18

1 Introduction

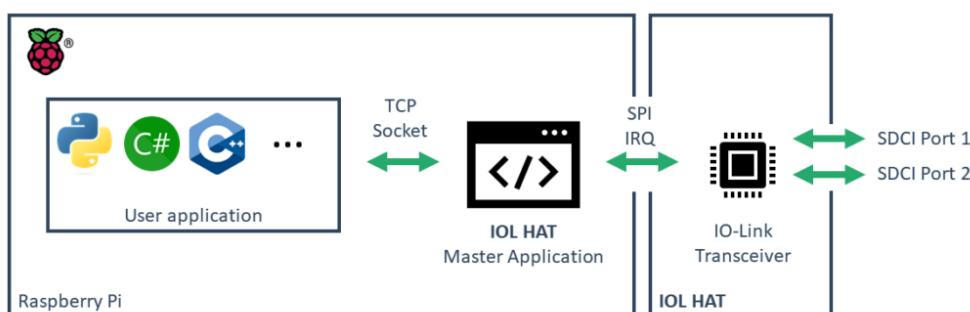
This section describes the methods and procedures that are used in connection with the open source software for the IOL HAT (“IOL master application”) which is available in the Github repository: <https://github.com/Pinetek-Networks/iol-hat>

The software is based on the I-Link stack by RT-Labs. Please note that the software has been proven to work in different applications, however, it comes without guarantees of specific functions or functionality.

Information on how to obtain the software and how to build it for specific targets can be found in the GitHub. The following sections describe the application of the software on the Raspberry Pi or other Single Board Computers.

Theory of operation

The master application handles the raw communication to the MAX14819 transceiver on the IOL-HAT over SPI and provides the stack routines to operate the IO-Link communication. The master application offers a TCP socket interface to the user application for easy integration. The master application and user application need to be run independantly.



Hints and Warnings

The following signs will guide you for dedicated areas of interest. The content is relevant for safe operation of the IOL HAT in combination with the software.



This symbol is used to show information that is relevant for the operation of the IOL HAT



This symbol is used to show areas of special attention that need to be considered for the safe operation of the IOL HAT

1.1 License and Disclaimer

Software License Notice

This software is released under the GNU General Public License v3.0 (GPL-3.0). License Terms

You have the freedom to use, modify, and distribute this software. Any derivative works must also be distributed under the GPL-3.0. The complete license text is available at:

<https://www.gnu.org/licenses/gpl-3.0.html>

Disclaimer of Liability

The software is provided "as is" without warranty of any kind, either expressed or implied. The authors or copyright holders shall not be liable for any claims, damages, or other liability arising from the use of the software. By using this software, you acknowledge that you have read and understood these terms.

2 IOL Master Application

To operate the IOL HAT on a Raspberry Pi, the master application binary from the IOL HAT repository is used: <https://github.com/Pinetek-Networks/iol-hat/bin>



Important note on the startup order: To work properly, the master application needs to be started AFTER 24V power is applied. Failure to do so will result in broken IO-Link communication.

The suitable master application file for the used Linux distribution shall be used. The Linux version can be determined with

```
uname -a
```

If the Linux distribution is not available as binary, please refer to the building instructions on the GitHub page to compile a matching version:

<https://github.com/Pinetek-Networks/iol-hat/tree/main/src-master-application>

2.1 Prepare the Target

The IOL HAT can be used with the binary `iol-master-appl` that is provided in the `/bin/` folder of the GitHub. This binary has been compiled with the aarch-linux toolchain for 64-bit version. For other versions and targets, please follow the instructions to build:

<https://github.com/Pinetek-Networks/iol-hat/blob/main/src-master-application/README.md>

The following instructions apply to Raspberry Pi, for other targets, please check with the corresponding instructions how to enable SPI and GPIO.

Enable SPI communication

The communication between Master Application `iol-master-appl` and the IOL HAT is done over SPI, so SPI communication needs to be enabled on the host. To enable SPI for Raspberry Pi hosts, the following instructions can be used:

```
-sudo raspi-config  
-go to: Interface Options  
-go to: SPI  
-select: Enable SPI
```

If you use another target, please verify with the documentation how to enable the SPI

GPIO

The interrupt line on between host and IOL HAT is a GPIO line. To operate, GPIOD is used as library. To install GPIOD, please use the following command

```
sudo apt install gpiod
```

2.2 Arguments and Timing

Command line arguments

```

-h, --help      shows help message and exits
-v, --version   prints version information and exits
-e, --extclock  Use clock for MAX14819 from ext source (not used for IOL
HAT)
-r, --realtime  Run realtime on given kernel (requires root rights, see
manual)
-i, --iolport   Specify the IOL port (12/34) (based on jumper settings on
the hardware, see manual), usage e.g., -i 34 for IOL port 3-4.
-t, --tcpport   Specify the TCP port, usage -t 1234 for port 1234.
Standard ports are 12010 for IOL port 1-2, and 12011 for IOL port 3-4

```

Timing considerations

IO-Link is a real-time protocol that requires fast response and cycle times to not run into timeouts. For running with sensors etc. that are queried 1-2 times per second, those timeouts are not critical. The iol-hat Master Application can run as user without further considerations.

If timeouts are critical (e.g., when HMI devices are connected that would change screen when timeouts occur), it is recommended to run the iol-hat in realtime mode. The solution is based on this description:

<https://forums.raspberrypi.com/viewtopic.php?t=228727>

As preparation, one core needs to be reserved. This is done by adding the this argument to /boot/firmware/cmdline.txt (for older versions of Raspberry OS, the file is /boot/cmdline.txt):

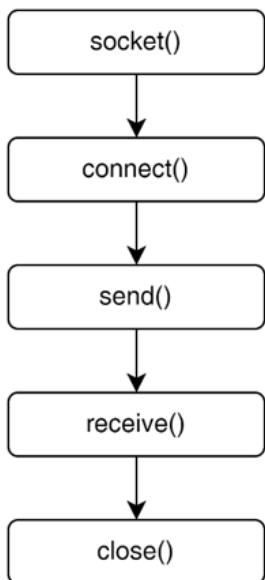
```
isolcpus=3
```

where 3 is the core you want to reserve (can be 0..3). The core needs to be matching with the option that you are giving for iol-hat, e.g., for core 3 it would be

```
iol-hat -r 3
```

3 Binary protocol

The sequence for communication over TCP is as follows:



The commands and responses are exchanged in the send() and receive() part of the sequence. The first octet in the command structure always defines the command ID.



If two IOL HAT are stacked, they share the same SPI data lines but different CS and IRQ lines. Which lines are used is set by jumpers on the hardware. The Master Application uses different TCP ports for addressing the port 1+3 setup and the port 2+4 setup (i.e. it controls different CS and IRQ lines).

Many commands use a port index (either 0 or 1). Port index 0 refers to the IOL HAT X1 connector, Port index 1 to IOL HAT X2 connector. Which IOL HAT module is addressed is determined through the TCP port as described above.

The commands have a common structure in the first two octets:

Octet #	Usage
0	Command ID
1	Length

In case of a command success, the return message as described in the commands is returned by the TCP server.

Error handling

In case of an error, the error message is returned:

Octet #	Usage
0	Error message ID = 0xFF
1	Error code

The following error codes are defined:

Error code	Usage
0x01	Message Length Error
0x02	Function ID unknown
0x03	Port power error, e.g. read while port disabled
0x04	Port ID error, i.e. port ID >1 called
0x05	Internal error, in this case octets 2..3 define the specific error code from the I-Link stack
0x06	Wrong status, e.g. data exchange when power off or no connection to the device

The error codes are listed in Appendix-A.

3.1 CMD_PWR

The power command switches the L+ power on the corresponding port. After switching on, the communication speed on the SDCI (COM1, COM2, COM3) is automatically detected and cyclic exchange of data is started.

Command:

Octet #	Usage
0	Command ID = 0x01
1	Port index 0x00 or 0x01
3	Power status 0x00 = OFF 0x01..0xFF=ON

Return Success:

Octet #	Usage
0	Command ID = 0x01
1	Port index 0x00 or 0x01
3	Power Status

3.2 CMD_LED

The LED command switches the LED on the corresponding port.

Command:

Octet #	Usage
0	Command ID = 0x02
1	Port index 0x00 or 0x01
3	LED Status (see below)

The LED status is 0x01 for the green LED and 0x02 for the red LED. With LED status 0x03, both LEDs are activated.

Return Success:

Octet #	Usage
0	Command ID = 0x02
1	Port index 0x00 or 0x01
3	LED Status

3.3 CMD_PD

This command exchanges the process data with the connected device. Process data is not validated in length or content.

Command:

Octet #	Usage
0	Command ID = 0x03
1	Port index 0x00 or 0x01
2	Length Out
3	Length In
4..	Data Out

The **Length Out** field command is length for the output process data. The structure and length of the output data can be found in the SDCI device's documentation. The **Length In** field needs to be set according to the device's documentation. If the length fields are not correctly set, this may lead to invalid data or loss of communication.

Return Success:

Octet #	Usage
0	Command ID = 0x03
1	Port index 0x00 or 0x01
2	Length Out (as given in the command)
3	Length In (as given in the command)
4..	Data In

3.4 CMD_READ

This command reads a parameter with the index and subindex as given on the corresponding port.

Command:

Octet #	Usage
0	Command ID = 0x04
1	Port index 0x00 or 0x01
2..3	Index, 16-bit value
4	Subindex
5	Length

The **Length** field is the desired/maximum length for the attribute. The structure of the attribute can be found in the SDCI device's documentation.

Return Success:

Octet #	Usage
0	Command ID = 0x0
1	Port index 0x00 or 0x01
2..3	Index, 16-bit value
4	Subindex
5	Read Length
6..	Read Data

The structure for the Read Data can be found in the SDCI device's documentation.

3.5 CMD_WRITE

This command writes a parameter with the index and subindex as given on the addressed port.

Command:

Octet #	Usage
0	Command ID = 0x05
1	Port index 0x00 or 0x01
2..3	Index, 16-bit value
4	Subindex
5	Length
6..	Write Data

The structure of the **Write Data** can be found in the SDCI device's documentation.

Return Success:

Octet #	Usage
0	Command ID = 0x04
1	Port index 0x00 or 0x01
2..3	Index, 16-bit value
4	Subindex
5	Length (as given in the command)

To verify the write operation, a [read operation](#) (Command ID 0x04) can be used on the same **Index** and **Subindex**.

3.6 CMD_STATUS

The status command returns the status of the corresponding port.

Command:

Octet #	Usage
0	Command ID = 0x06
1	Port index 0x00 or 0x01

Return Success:

Octet #	Usage	Comment
0	Command ID = 0x06	
1	Port index 0x00 or 0x01	
2	Process data IN valid	
3	Process data OUT valid	0x00 = Not valid 0x01 = Valid
4	Transmission rate	0x00 = Detection Failure 0x01 = COM1 (4.8 kbps) 0x02 = COM2 (38.4 kbps) 0x03 = COM3 (230.4 kbps)
5	Process data IN length	
6	Cycle time	
7	Process data OUT length	
8..9	Vendor ID	16-bit value
10..13	Device ID	32-bit value
14	Power	0x00 = Power OFF 0x01 = Power ON

Appendix-A Error messages

The following extended Error Messages are defined (see iolink.h in application source code):

```

IOLINK_SMI_ERRORTYPE_NONE = 0x0000,

/* Table C.1 ErrorTypes */
IOLINK_SMI_ERRORTYPE_APP_DEV = 0x8000,
IOLINK_SMI_ERRORTYPE_IDX_NOTAVAIL = 0x8011,
IOLINK_SMI_ERRORTYPE_SUBIDX_NOTAVAIL = 0x8012,
IOLINK_SMI_ERRORTYPE_SERV_NOTAVAIL = 0x8020,
IOLINK_SMI_ERRORTYPE_SERV_NOTAVAIL_LOCCTRL = 0x8021,
IOLINK_SMI_ERRORTYPE_SERV_NOTAVAIL_DEVCTRL = 0x8022,
IOLINK_SMI_ERRORTYPE_IDX_NOT_ACCESSIBLE = 0x8023,
IOLINK_SMI_ERRORTYPE_PAR_VALOUTOFRNG = 0x8030,
IOLINK_SMI_ERRORTYPE_PAR_VALGTLIM = 0x8031,
IOLINK_SMI_ERRORTYPE_PAR_VALLTLIM = 0x8032,
IOLINK_SMI_ERRORTYPE_VAL_LEN0VRRUN = 0x8033, /* Also in C.2 */
IOLINK_SMI_ERRORTYPE_VAL_LENUNDRUN = 0x8034,
IOLINK_SMI_ERRORTYPE_FUNC_NOTAVAIL = 0x8035,
IOLINK_SMI_ERRORTYPE_FUNC_UNAVAILTEMP = 0x8036,
IOLINK_SMI_ERRORTYPE_PAR_SETINVALID = 0x8040,
IOLINK_SMI_ERRORTYPE_PAR_SETINCONSIST = 0x8041,
IOLINK_SMI_ERRORTYPE_APP_DEVNOTRDY = 0x8082,
IOLINK_SMI_ERRORTYPE_UNSPECIFIC = 0x8100,

/* Table C.2 Derived ErrorTypes */
IOLINK_SMI_ERRORTYPE_COM_ERR = 0x1000,
IOLINK_SMI_ERRORTYPE_I_SERVICE_TIMEOUT = 0x1100,
IOLINK_SMI_ERRORTYPE_M_ISDU_CHECKSUM = 0x5600,
IOLINK_SMI_ERRORTYPE_M_ISDU_ILLEGAL = 0x5700,

/* Table C.3 SMI related ErrorTypes */
IOLINK_SMI_ERRORTYPE_ARGBLOCK_NOT_SUPPORTED = 0x4001,
IOLINK_SMI_ERRORTYPE_ARGBLOCK_INCONSISTENT = 0x4002,
IOLINK_SMI_ERRORTYPE_DEV_NOT_ACCESSIBLE = 0x4003,
IOLINK_SMI_ERRORTYPE_SERVICE_NOT_SUPPORTED = 0x4004,
IOLINK_SMI_ERRORTYPE_DEV_NOT_IN_OPERATE = 0x4005,
IOLINK_SMI_ERRORTYPE_MEMORY_OVERRUN = 0x4006,
IOLINK_SMI_ERRORTYPE_PORT_NUM_INVALID = 0x4011,
IOLINK_SMI_ERRORTYPE_ARGBLOCK_LENGTH_INVALID = 0x4034,
IOLINK_SMI_ERRORTYPE_SERVICE_TEMP_UNAVAILABLE = 0x4036,

```